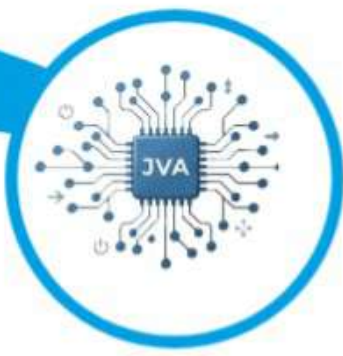




JVA Internships

Professional Training Programs

Design Verification

Build Strong Functional Verification Skills

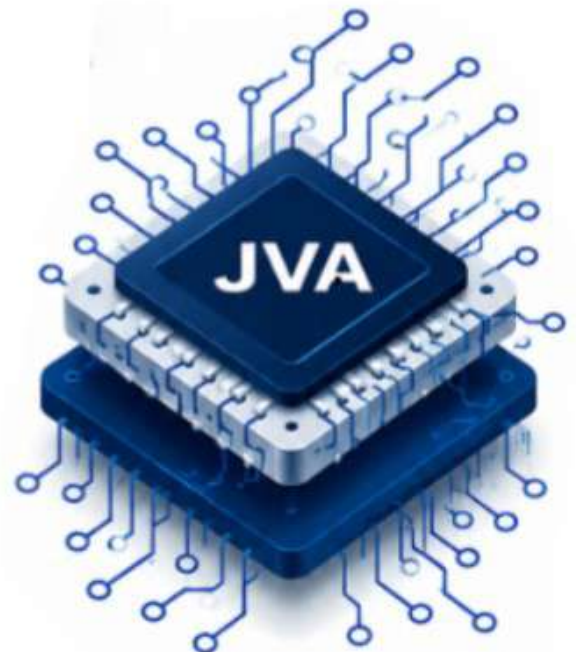
Gain hands-on experience in modern chip verification methodologies,

What You Will Learn

- Verilog & SystemVerilog Basics
- UVM Methodology
- Testbench Development
- Assertions & Coverage
- Protocol Verification
- Simulation & Waveform Analysis
- Debugging Techniques
- Functional Verification Flow

Program Highlights

- Real-Time Verification Projects
- Hands-On Simulation Practice
- Industry-Oriented Workflow
- Mentor Support
- Interview Preparation



Who can Apply?

- ECE / EEE Students
- B.Tech / M.Tech Graduates
- VLSI Enthusiasts
- Freshers Looking for Semiconductor Careers

Why Choose This Program?

- Learn Industry Verification Standards
- Practical Learning Approach
- Strong Foundation in Functional Verification
- Exposure to Real-Time Projects
- Career Guidance & Placement Support



INTERNSHIP OUTCOMES

- Understand complete DV flow
- Build verification testbenches
- Analyze simulation results
- Work on real-time verification tasks
- Prepare for DV interviews

 *Pinakin Spaces, Sector III HMDA, Madhapur, Hyderabad*