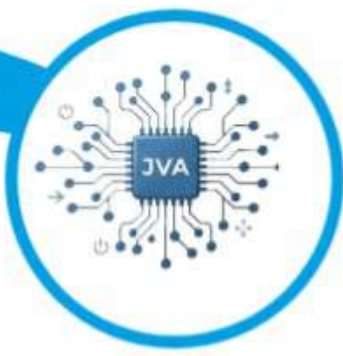




JVA Internships

Professional Training Programs

PHYSICAL DESIGN

Build Industry-Ready VLSI Backend Skills

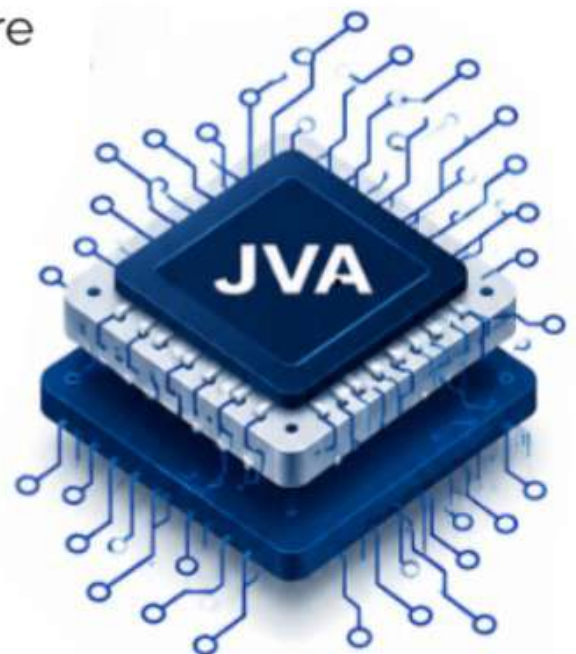
Gain hands-on experience in complete ASIC Physical Design flow used in semiconductor industries.

What You Will Learn

- Introduction to VLSI & Semiconductor Industry
- Linux & TCL Basics
- ASIC Design Flow Overview
- Floorplanning
- Power Planning
- Placement
- Clock Tree Synthesis (CTS)
- Routing
- Timing Analysis & Timing Closure
- Physical Verification (DRC/LVS)
- ECO Flow & Signoff Concepts

Program Highlights

- Live Projects Based Training
- Real-Time Industry Workflow
- Hands-on Lab Sessions
- Mentor Support
- Interview Preparation



Who can Apply?

- ECE / EEE Students
- B.Tech / M.Tech Graduates
- VLSI Enthusiasts
- Freshers Looking for Semiconductor Careers

Why Choose This Program?

- Learn from Industry Professionals
- Practical Learning Approach
- Strong Foundation in Backend VLSI
- Exposure to Real Chip Design Concepts
- Career Guidance & Placement Support



INTERNSHIP OUTCOMES

- Understand complete Physical Design flow
- Work on backend implementation tasks
- Analyze timing and routing issues
- Handle basic ASIC implementation projects
- Prepare for VLSI Physical Design interviews

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